

FIGURE 1



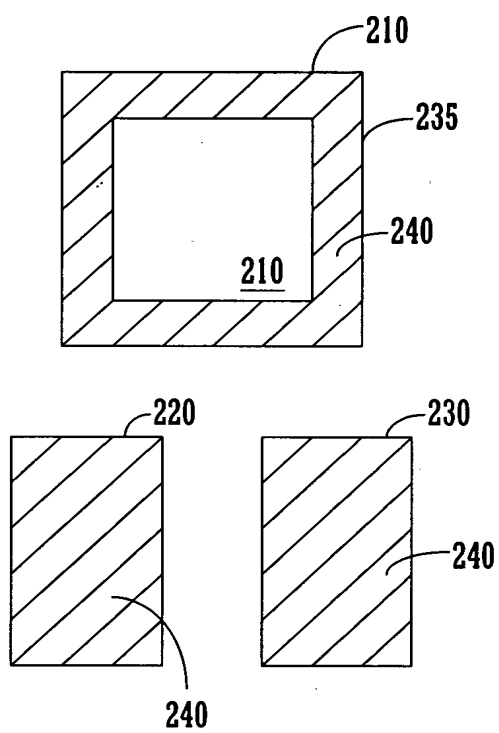


FIGURE 2A

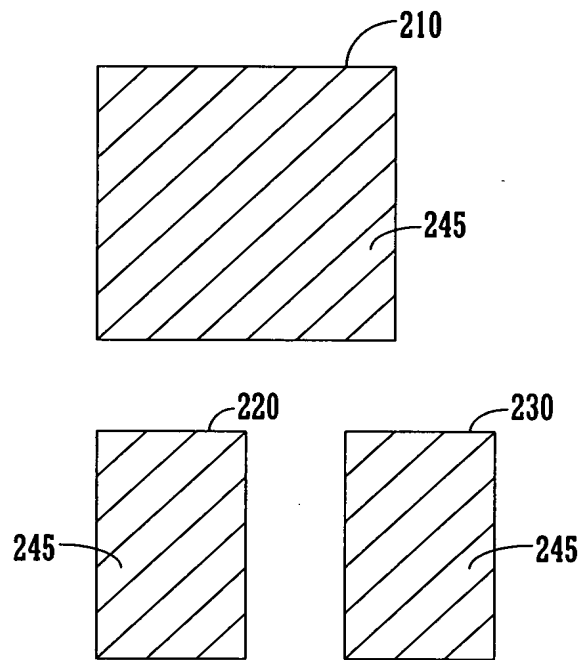


FIGURE 2B

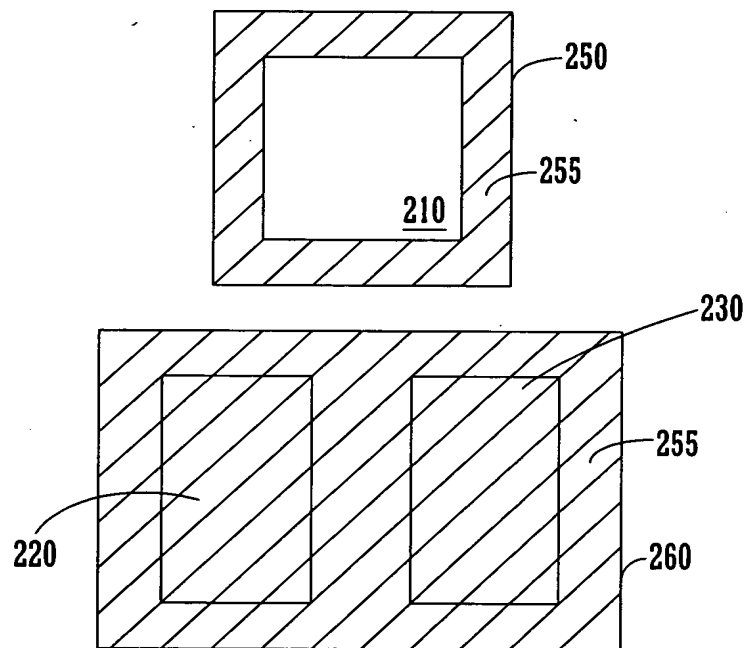


FIGURE 2C

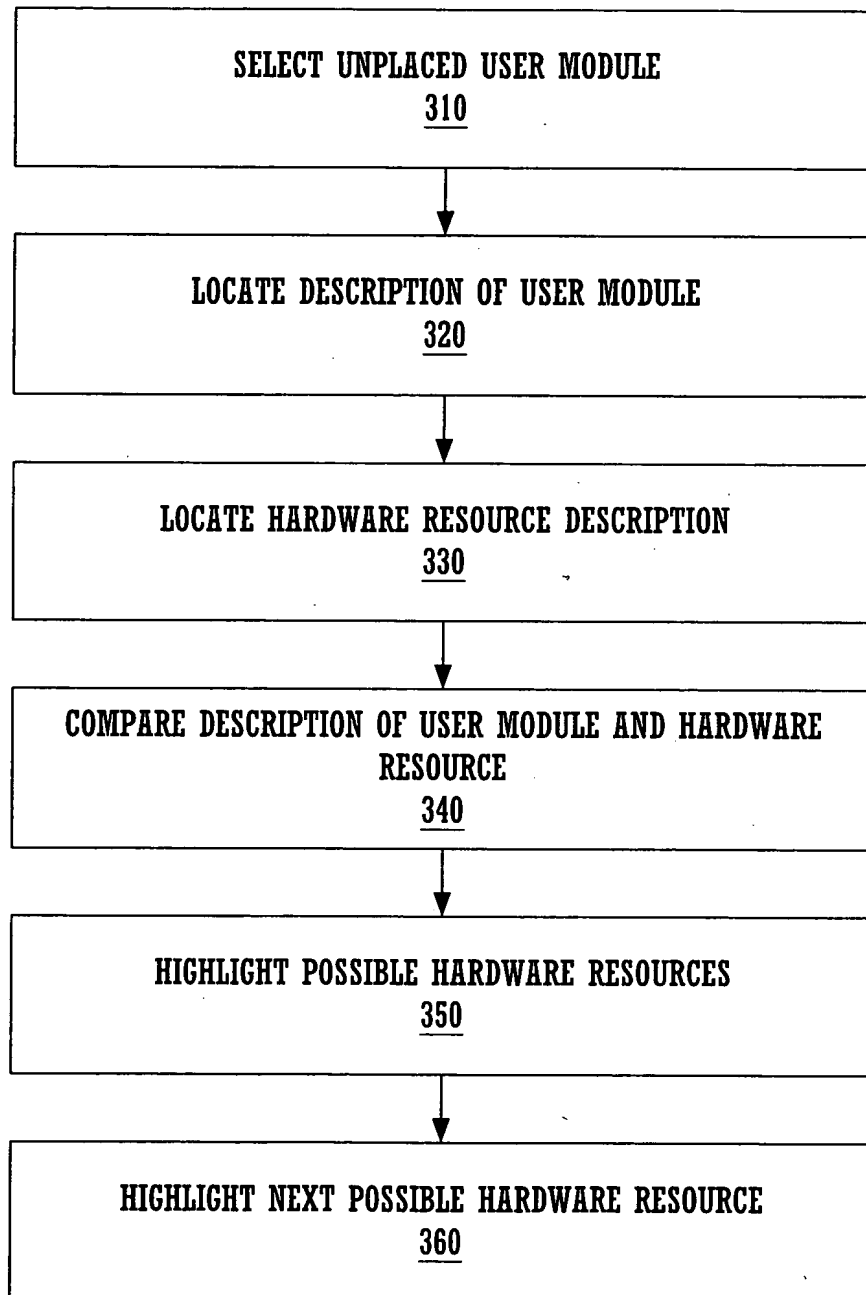


FIGURE 3

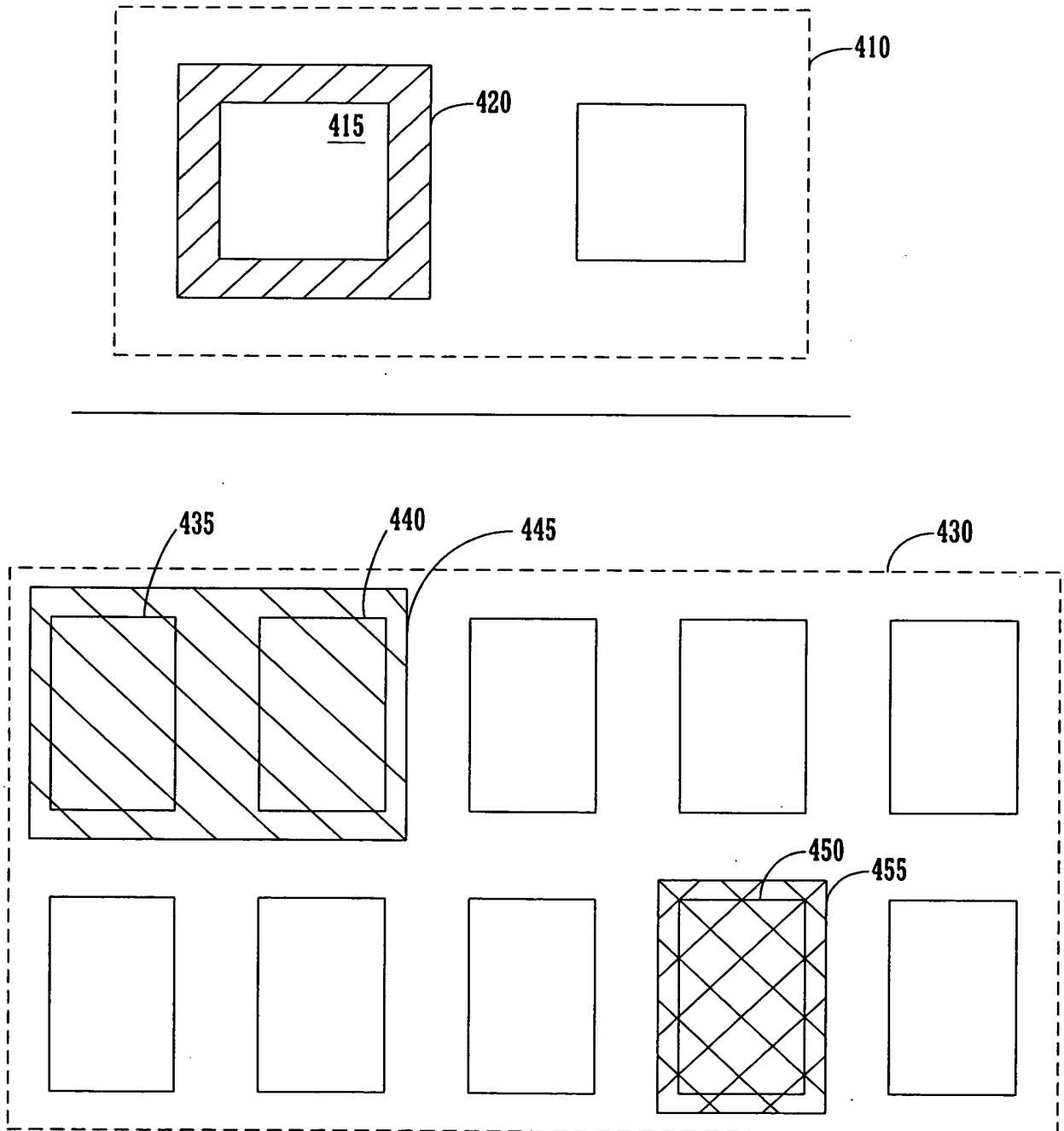


FIGURE 4

USER MODULES SELECTED FOR PLACEMENT

CPU_Clock	ADCINC12_1	INSAMP_1	INSAMP_2	PWM16_1	UART_1
32K_Select	Counter16_1	DAC8_1			
PLL_Mode					
Sleep_Timer					
24V1=24MHz/N					
24V2=24V1/N					
Analog Power					
Ref Mux					
Op-Amp Bias					
A_Buff_Power					
SwitchModePump					
VoltMon_Range					
VoltMon_Threshold					

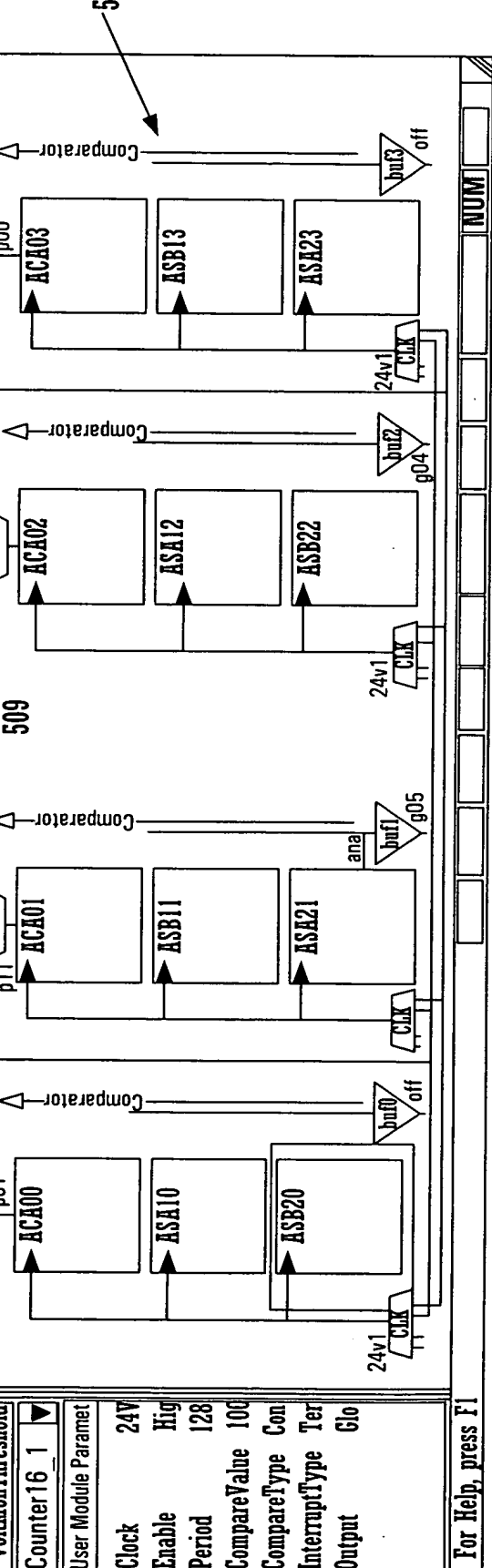
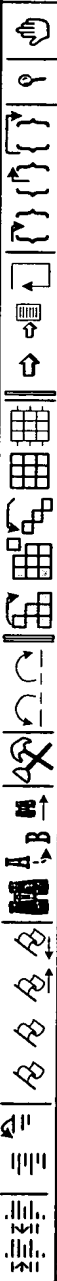


FIGURE 5A



USER MODULES SELECTED FOR PLACEMENT

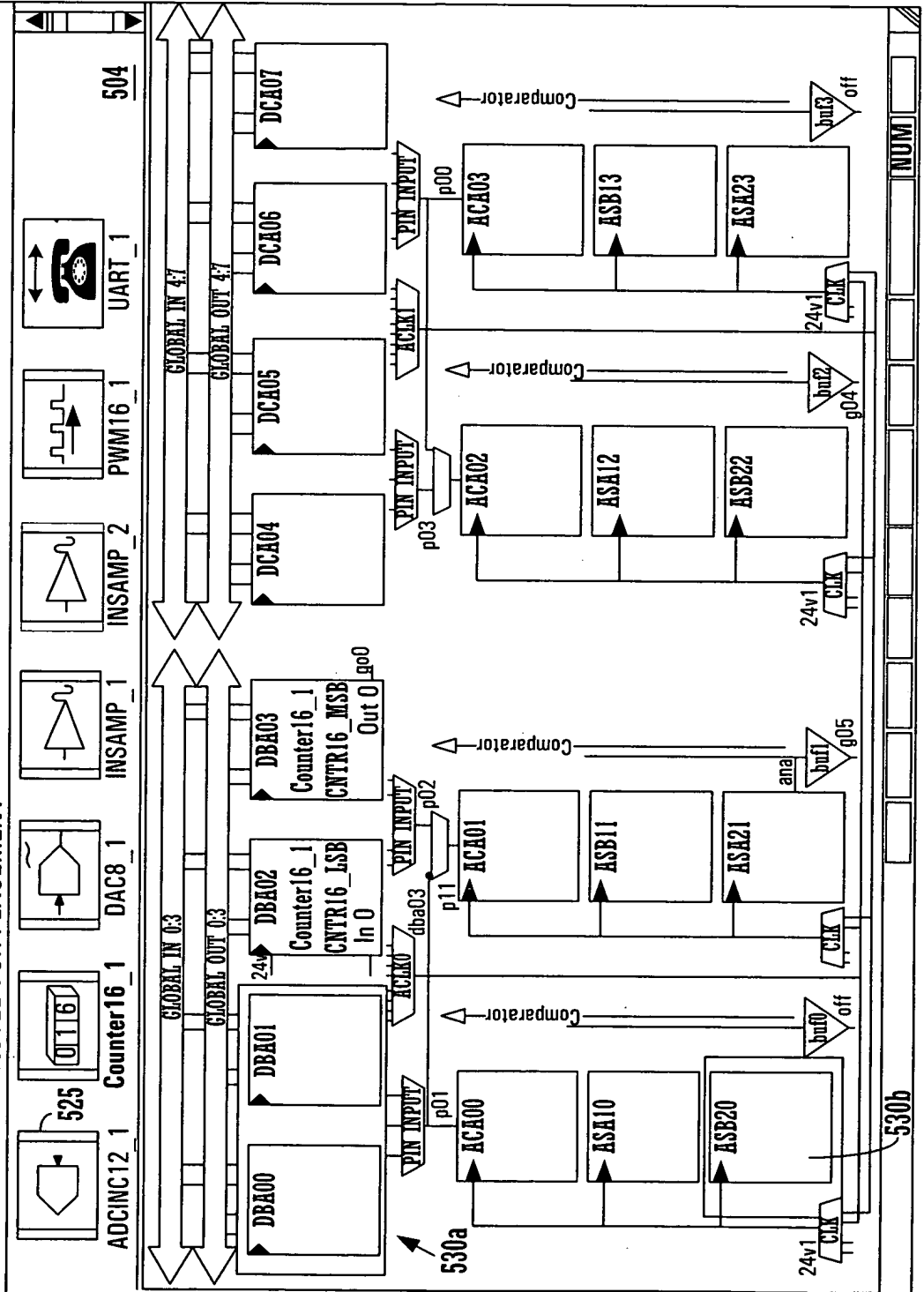


FIGURE 5B

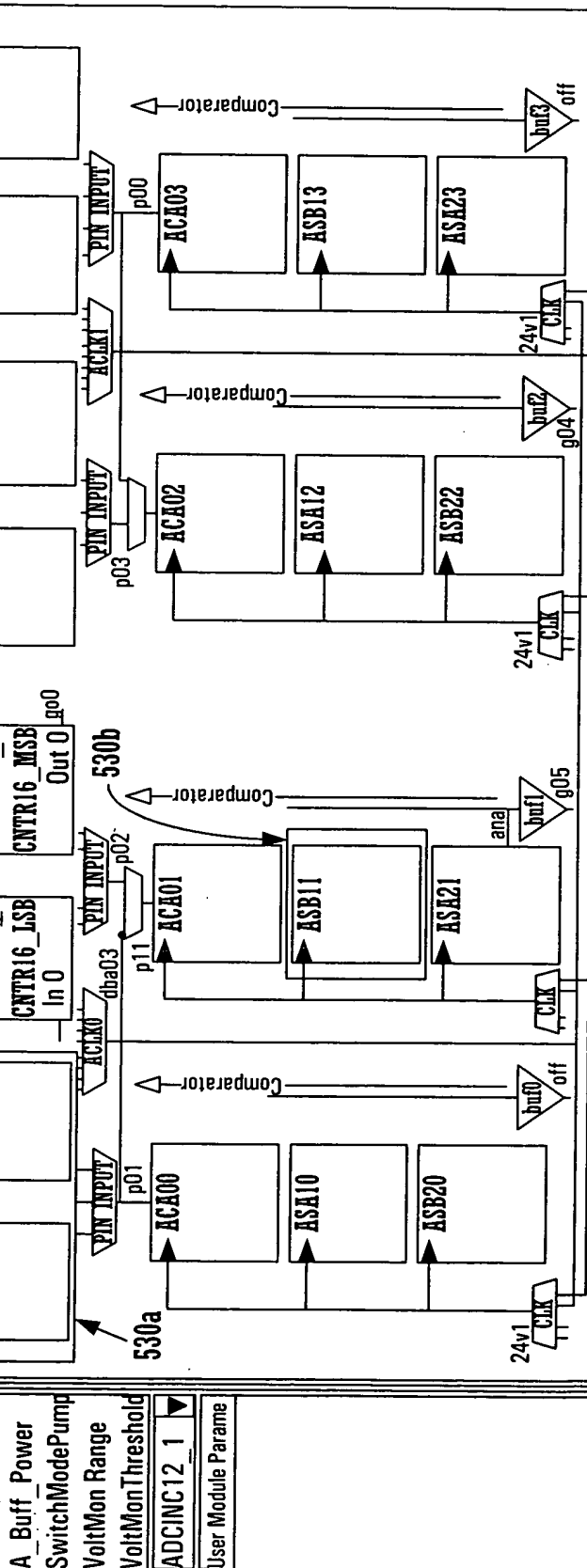
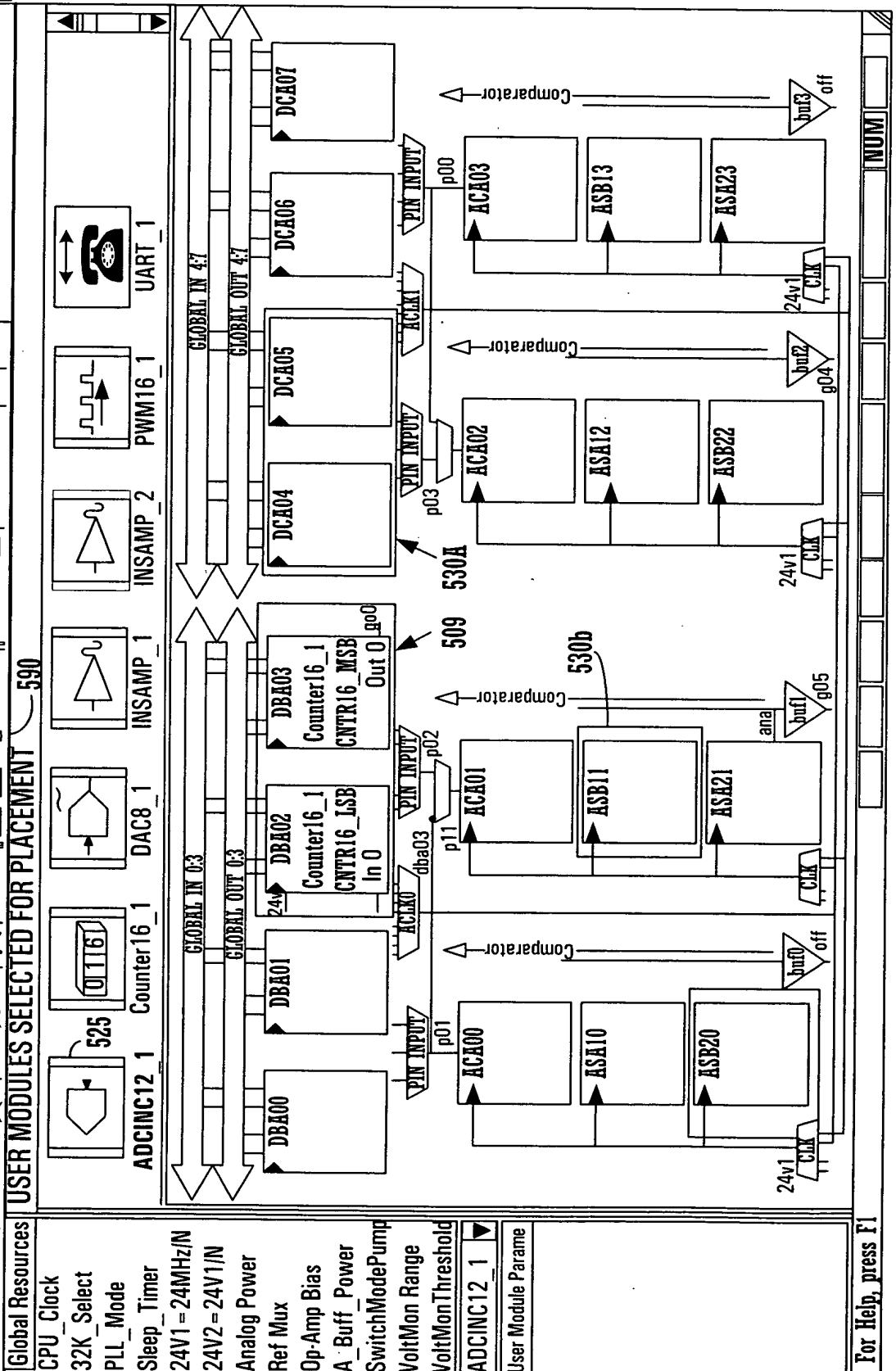


FIGURE 5C

FIGURE 5D



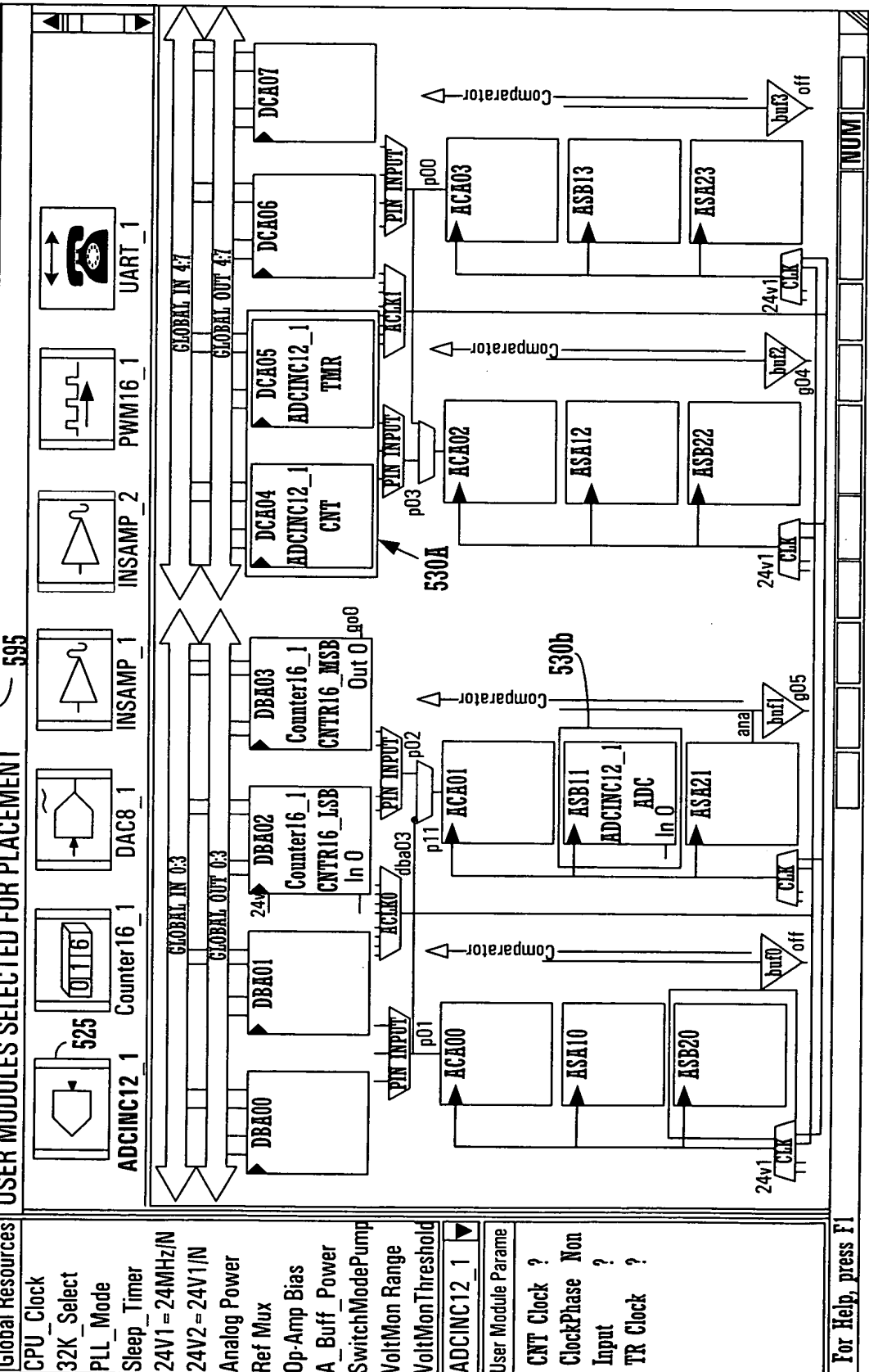


FIGURE 5E